

# CASES 2012

International Conference on Compilers  
Architecture and Synthesis for Embedded Systems

Tampere Finland ♦ 7-12 October 2012

<http://esweek.acm.org/cases/>

**CASES** is a forum where researchers, developers and practitioners exchange information on emerging technologies and the latest advances in compilers and architectures for high performance embedded systems design and synthesis. CASES uniquely aims to promote synergies across otherwise vertically integrated communities in embedded systems.

Previously unpublished papers containing significant novel ideas and technical results are solicited in the following areas:

- models of computation and compilers for heterogeneous systems including GPUs and FPGAs
- heterogeneous system-on-chip
- on-chip communication architectures
- extensible, customizable ASIPs
- run-time and design time reconfigurable processors
- novel nano-based architectures
- 3D architectures, integration and synthesis
- domain specific embedded applications
- memory management
- smart caches and compiler controlled memories
- static and dynamic timing analysis
- specification and design of embedded systems
- compilation and/or synthesis for reliability, power, performance
- modeling & management for reliability, power, performance
- validation, verification, & debugging of embedded software
- analysis techniques for embedded system including design space exploration, co-simulation

## Important dates

- Abstract Submission March 28
- Paper Submission April 4
- Acceptance Notification July 3

## Co-located events

- CODES+ISSS
- ESTIMedia
- EMSOFT
- NexIENet
- SoC
- And more

## Conference Chairs

Ahmed Jerraya  
Luca Carloni

## Program Chairs

Vincent Mooney  
Rodric Rabbah

## Program Committee

M. Balakrishnan  
Oliver Bringmann  
Srihari Cadambi  
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Frank Mueller  
Stephen Neuendorffer  
Walid Najjar  
Laura Pozzi  
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Muhammad Shafique  
Tim Sherwood  
Sunil Shukla  
Aviral Shrivastava  
Michael Taylor  
Lothar Thiele  
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Jingling Xue  
Sami Yehia